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APPLICATION NO.	FILING D	DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/666,462	09/19/2	2003	Brent Stone	42P16890	8351	
8791	7590	06/20/2006		EXAMINER		
BLAKELY SOKOLOFF TAYLOR & ZAFMAN				HYEON, HAE M		
12400 WILS SEVENTH	SHIRE BOULE' FLOOR	VARD		ART UNIT	PAPER NUMBER	
	LES, CA 9002	25-1030		2839		
				DATE MAILED: 06/20/200	6	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)						
	10/666,462	STONE ET AL.	, .					
Office Action Summary	Examiner	Art Unit						
	Hae M. Hyeon	2839						
The MAILING DATE of this communication app	ears on the cover sheet with the c	orrespondence address						
Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 16(a). In no event, however, may a reply be tim ill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	I. lely filed the mailing date of this communication O (35 U.S.C. § 133).						
Status								
1)⊠ Responsive to communication(s) filed on 20 Ap	oril 2006							
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closed in accordance with the practice under E								
Disposition of Claims		·						
<u> </u>			•					
•	Claim(s) <u>1-16</u> is/are pending in the application.							
5) Claim(s) is/are allowed.	4a) Of the above claim(s) is/are withdrawn from consideration.							
6)⊠ Claim(s) <u>1-16</u> is/are rejected.								
7) Claim(s) is/are objected to.								
8) Claim(s) are subject to restriction and/or	election requirement.							
	•							
Application Papers	•							
9) The specification is objected to by the Examine		_						
10) ☐ The drawing(s) filed on is/are: a) ☐ acce								
Applicant may not request that any objection to the	•		-1\					
Replacement drawing sheet(s) including the correcting 11) The oath or declaration is objected to by the Ex			a).					
TT) The bath of declaration is objected to by the Ex	ammer. Note the attached Office	Action of form FTO-132.						
Priority under 35 U.S.C. § 119								
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:	priority under 35 U.S.C. § 119(a)	-(d) or (f).						
1. Certified copies of the priority documents have been received.								
2. Certified copies of the priority documents	·							
Copies of the certified copies of the prior		ed in this National Stage						
application from the International Bureau	• • • • • • • • • • • • • • • • • • • •							
* See the attached detailed Office action for a list	of the certified copies not receive	ed.						
	•	•						
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Attachment(s)								
1) Notice of References Cited (PTO-892)	4) Interview Summary							
 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) 	Paper No(s)/Mail Da 5) Notice of Informal P	ate Patent Application (PTO-152)						
Paper No(s)/Mail Date	6) Other:							

Application/Control Number: 10/666,462

Art Unit: 2839

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-6 and 8-16 are rejected under 35 U.S.C. 102(b) as being anticipated by Katz (US 6,310,398 B1).

In the "BACKGROUND OF THE INVENTION," Katz discloses that an integrated circuit (IC) package commonly has interface schemes Pin grid Array (PGA), Ball Grid Array (BGA), and Land Grid Array (LGA) and a plurality of input/output (I/O) conductors. Also, the IC package is mounted on a printed circuit board (PCB) having a matching pattern of pins or pads on the IC package. Furthermore, the IC package can usually interface with a socket (connector) that is mounted on the PCB. In Figures 12B, 12C and 14-18, Katz discloses IC package having a plurality of I/O pins or lands with varied horizontal and vertical pitch distances.

Claim Rejections - 35 USC § 103

- 3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

4. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Katz in view of Kajinuma (US 6,722,910 B2).

Claim 7 recites that the connector is a zero insertion force (ZIF) connector. While Katz does not disclose a ZIF connector, Kajinuma discloses a ZIF connector receiving an IC package. Furthermore, Kajinuma explains that the ZIF connector is well known for reducing the insertion force that must be applied to the pins of the IC package during the mounting of the IC package to a PCB (see column 1, lines 18-23).

It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify the connector taught by Katz such that it would be a ZIF connector as taught by Kajinuma to reduce the insertion force that must be applied to the pins of the IC package during the mounting of the IC package to a PCB.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. US Patent No. 6,664,620 B2 by Siu et al.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hae M. Hyeon whose telephone number is 571-272-2093. The examiner can normally be reached on Mon.-Fri. (8:30-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tulsidas C. Patel can be reached on (571) 272-2098. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/666,462

Art Unit: 2839

Page 4

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Hae M Hyeon Primary Examiner Art Unit 2839

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Hae Moon Hyeon